**Universitatea Tehnică din Cluj-Napoca**
**Facultatea de Electronică, Telecomunicaţii şi Tehnologia Informaţiei**
**Departamentul de Bazele Electronicii**
**Șef Lucrări dr.ing. Oneţ Raul-Ciprian**

# LISTA
## lucrărilor ştiinţifice în domeniul disciplinelor din postul didactic

## A – Teza de doctorat

*„Baseband signal processing for integrated radio receivers”*
Conducător ştiinţific : Prof.dr.ing. Ţopa Marina
Domeniul.Inginerie Electronică şi Telecomunicatii
Universitatea.Tehnică din Cluj-Napoca
Susţinere publică: *30 septembrie 2011*.

## B – Cărţi şi capitole în cărţi publicate în ultimii 10 ani

**CĂRŢI**

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**CAPITOLE DE CARTE**

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## C – Lucrări indexate ISI/BDI publicate în ultimii 10 ani

**c1) Articole / studii publicate în reviste de specialitate de circulaţie internaţională recunoscute (cotate ISI)**

1. O’Mahony, T., Hill, M., **Onet, R**., Neag, M., Torre Cubillo, L. D. L., & Zhou, D. (2024). The impact of take-home laboratories on student perceptions of conceptual and professional learning in electronic engineering across four European universities. European Journal of Engineering Education, 49(6), 1376-1396.
2. **ONET, Raul**, Marius NEAG, Albert FAZAKAS, Paul MIRESAN, Gabriel PETRASUC, Iulian SULAREA, Alessandro BATTIGELLI, Michael MURRAY, and Martin HILL. "A Blended On-Campus and At-Home Approach to Laboratories on Electronic Circuits." SCIENCE AND TECHNOLOGY 26, no. 2 (2023): 167-180.
3. Beleca, V., Plesa, C. S., **Onet, R.**, & Neag, M. (2022). Methods for Assessing the Stability of Conditionally Stable Circuits by Using Small-signal Simulations. ROMANIAN JOURNAL OF INFORMATION SCIENCE AND TECHNOLOGY, 25(2), 205-223.
4. Neag, M., Kovács, I., **Oneț, R**., & Câmpanu, I. (2021). Design options for high-speed OA-based fully differential buffers able to drive large loads. Microelectronics Journal, 114, 105115.

5. Miresan, Paul, **Raul Onet**, Marius Neag, Marina Topa, and Cosmin Chira. "Design options for implementing in standard CMOS drivers for MEMS body biasing." Microelectronics Journal 97 (2020): 104705.
6. **Onet, Raul**, Ana Rusu, and Saul Rodriguez. "High-Purity and Wide-Range Signal Generator for Bioimpedance Spectroscopy." IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 65, Issue 12, 2018, pp. 1884–1888.
7. **Onet, Raul**; Neag, Marius; Kovacs, Istvan; Topa, Marina Dana; Rodriguez, Saul; Rusu, Ana. Compact Variable Gain Amplifier for a Multistandard WLAN/WiMAX/LTE Receiver. IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I-REGULAR PAPERS, vol. 61, Issue 1, 2014, pp. 247–257, ISSN 1549-8328.
8. Neag, Marius; **Onet Raul**; Kovacs, Istvan; Martari, Paul. Comparative Analysis of Simulation-Based Methods for Deriving the Phase- and Gain- Margins of Feedback Circuits with OpAmps. *IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I-REGULAR PAPERS*, vol. 62, Issue 3, 2015, pp. 625–634; ISSN 1549-8328.

**Articole / studii publicate în reviste de specialitate de circulaţie internaţională recunoscute (indexate BDI)**

9. O'Mahony, T., Murray, M., Hill, M., **Onet, R.**, Neag, M., de la Torre Cubillo, L., & Zhou, D. (2024). A Take Home Laboratory to Support Teaching Electronics:: Instructors Perspectives and Technical Revisions. Journal on Teaching Engineering, 3(1), 15-29.
10. Salajan, T. F., Campanu, I., **Oneţ, R.**, Neag, M., & Ţopa, M. (2018). ANALYSIS AND COMPARISON OF UNIVERSAL gm-C BIQUAD STRUCTURES. Acta Technica Napocensis, 59(2), 22-27.

**c2) Studii publicate la conferinţe indexate în baze de date internaţionale de referinţă în domeniul *inginerie electronică şi telecomunicaţii* (DBLP, ACM, IEEE, SCOPUS etc.)[1]**

11. Goia, Iulian-Teodor, Iulian Câmpanu, Marius Neag, and **Raul Oneţ**. "Study on Minimizing Noise in Several CMOS Bandgap Voltage References." In 2024 International Semiconductor Conference (CAS), pp. 233-236. IEEE, 2024. **[IEEE]**.
12. Lupoi, Victorina, Tomina Fabiola Sălăjan, **Raul Oneţ**, and Marius Neag. "Current Recycling-Based LDO for Large Load Applications." In 2024 International Semiconductor Conference (CAS), pp. 245-248. IEEE, 2024. **[IEEE]**.
13. Moldovan, Petru-Rares, George-Gabriel Simeon, Cosmin-Sorin Plesa, **Raul-Ciprian Onet**, and Marius Neag. "A Novel Class-AB Buffer for LDOs with High Accuracy and Fast Transients." In 2024 International Semiconductor Conference (CAS), pp. 217-220. IEEE, 2024. **[IEEE]**.
14. Battigelli, Alessandro, **Raul Onet**, Marius Neag, and Cosmin Sorin Plesa. "A super Class AB OTA with High Slew-Rate for Automotive LDOs." In 2023 International Semiconductor Conference (CAS), pp. 183-186. IEEE, 2023. **[IEEE]**.

[1] indexate în:
[IEEE] - IEEE Xplore (http://ieeexplore.ieee.org/Xplore/guesthome.jsp )
[ACM] - ACM portal (http://portal.acm.org )
[DBLP] - (http://www.informatik.uni-trier.de )
[SCOPUS] - (http://www.scopus.com)

15. Marian, Elena-Andreea, Mădălina-Daniela Farcaș, and **Raul-Ciprian Oneț**. "Comparison of Three Overcurrent Protection Circuits for LDOs." In 2023 International Semiconductor Conference (CAS), pp. 171-174. IEEE, 2023. **[IEEE]**.
16. Sergiu-Andrei, Parfenov, **Raul Oneț**, and Simeon Gabriel. "Over/Under Voltage and Thermal Shutdown protections for LDOs used in PMICs: Design techniques." In 2023 International Semiconductor Conference (CAS), pp. 241-244. IEEE, 2023. **[IEEE]**.
17. **Onet, Raul**, Marius Neag, Albert Fazakas, Paul Miresan, Gabriel Petrasuc, Iulian Sularea, Alessandro Battigelli, and Martin Hill. "A Home Electronic Laboratory for Each Student-A Potential Paradigm Shift in Teaching Electronics." In 2022 International Semiconductor Conference (CAS), pp. 141-147. IEEE, 2022 **[IEEE]**.
18. Gheorghiță, Emilia, **Raul Oneț**, and Iulian Câmpanu. "Comparison of three bandgap topologies implemented in standard CMOS technology with parasitic PNP bipolars." In 2022 International Semiconductor Conference (CAS), pp. 217-220. IEEE, 2022. **[IEEE]**
19. Grigorita, Elena, Tomina Fabiola Sălăjan, and **Raul Onet**. "Novel Thermal Shutdown Circuit for Automotive Power Management Application." 2022 International Semiconductor Conference (CAS). IEEE, 2022. **[IEEE]**
20. Adrian Bojita; **Raul Onet**; Marius Neag; Marius Purcar; Vasile Topa. Modeling Thermally-Induced Mechanical Faults in Bond Wire-Pad Assemblies. 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME). **[IEEE]**.
21. Chaourani, P., Hellstrom, P. E., Rodriguez, S., **Onet, R.,** & Rusu, A. (2017, March). Enabling area efficient RF ICs through monolithic 3D integration. In 2017 Design, Automation & Test in Europe Conference & Exhibition (DATE) (pp. 610-613). IEEE. DOI: 10.23919/DATE.2017.7927059 PROCEEDINGS OF THE 2017 DESIGN, AUTOMATION & TEST IN EUROPE CONFERENCE & EXHIBITION. **[SCOPUS] ], [IEEE].**

## D – Lucrări publicate în ultimii 10 ani în reviste şi volume de conferinţe cu referenţi (neindexate)

- Selecţie cu maximum 20 lucrări în volume de conferinţe

1. **Oneţ, Raul**, Rusu, Ana. “Energy autonomy analysis of single-channel EEG recording systems”. IEEE Life Sciences Conference, LSC 2017, Sydney, Australia, 13-15 December 2017

## E – Brevete obţinute pentru întreaga activitate

-

## F – Contracte de cercetare (2020-2025)

1. 2024-2025: Director proiect nr. 4310/12.02.2024, „Dezvoltarea unor circuite integrate pentru managementul puterii cu performanțe îmbunătățite”, contract de cercetare cu incheiat de UTCN cu firma Infineon Technologies Romania SCS
2. 2023-2024: Director proiect nr. 7247/14.03.2023, „Proiectarea și optimizarea unor blocuri funcționale destinate circuitelor integrate de mare performanță pentru managementul puterii”, contract de cercetare cu incheiat de UTCN cu firma Infineon Technologies Romania SCS

3. 2021-2023: Responsabil proiect partener proiect „HELP – Home Electronics Laboratory Platform”, ERASMUS+ Programme 2020 – KA2 High Education; proiect nr.: 2020-1-IE02-KA226-HE-000786
4. 2021 – 2022: Membru în contract de cercetare “Tracker”, contract nr. 39024/23/12/2021, cu terti interni.

Data
17.01.2025

Semnătura